

Buck-based DMPPT emulator: a helpful experimental demonstration unit

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ABSTRACT

Distributed control strategy represents the most promising solution to enhance the lackluster energetic performance of mismatched PhotoVoltaic (PV) systems. Moreover, many factors that contribute to such poor performance are still to be explored. To fully understand the advantages offered by the Distributed Maximum Power Point Tracking (DMPPT) approach, the implementation of a DMPPT emulator is necessary. Based on the above needs, this paper describes the realization and use of a Buck-based DMPPT emulator and shows its high flexibility and potential. The realized device is capable to emulate the output current vs. voltage (I - V) characteristics of many commercial PV modules with a dedicated Buck DC/DC converter not only in controlled atmospheric conditions but also with different currents rating of the switching devices. The system implementation is based on a commercial power supply controlled by a low-cost Arduino board. Data acquisition is performed through a low-cost current and voltage sensor by using a multichannel board by National Instruments. Experimental results confirm the validity and potential of the proposed DMPPT emulator.

Section: RESEARCH PAPER

Keywords: Distributed maximum power point tracking; mismatching; PV emulator

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1. INTRODUCTION

The world is currently in a dramatic climate crisis due to the growth of greenhouse gas emissions. To reduce climate change, a sustainable energy transition, oriented to the achievement of the carbon neutrality, is needed. In this direction, the total production of electricity, using renewable energy sources, represents the main goal.

To increase the distributed green energy generation, PhotoVoltaic (PV) systems are among the most promising renewable sources. In PV applications, the heaviest working conditions, not only from an energetic efficiency point of view [1]-[5], but also from a reliability one [6]-[8], are known as "mismatching conditions". In particular, when mismatching conditions occur, due to clouds, shadows, dirtiness and/or different orientation of PV modules etc., the commonly used grid-connected PV systems, made of string of PV modules connected in parallel and feeding a central inverter (Figure 1), are ineffective [1]-[8]. Various Maximum Power Point Tracking (MPPT) control techniques have been proposed in the literature to track the MPP and force the system to work at the MPP point, including high-performance MPPT techniques [9]-[19],

reconfiguration architectures and algorithms, [20]-[29] and Distributed MPPT (DMPPT) approach [30]-[41]. Among of all these techniques, the DMPPT (Figure 2) is by far the most promising solution to enhance the reduced energetic performance of mismatched PV systems [30]-[41]. Differently from the Centralized approach of Figure 1, DMPPT uses a MPPT Module Dedicated DC/DC Converter realizing the MPPT for each PV module. Nevertheless, there are still several factors limiting such performance, including the efficiency of the power stage, constraints imposed by the topology, the finite rating of silicon devices, atmospheric conditions, and the suboptimal value of string voltage [39]-[43]. The fact that many of these factors are not under our control represents a severe restriction in conducting experimental test activities when real DMPPT PV systems are considered. What emerges is the necessity of the implementation of a DMPPT emulator as a helpful tool to emulate the output I - V characteristics of many commercial PV modules with a dedicated Buck DC/DC converter not only in controlled environment conditions (as it happens for common PV emulators [42]-[45]) but also with different currents rating of the switching devices. Based on the

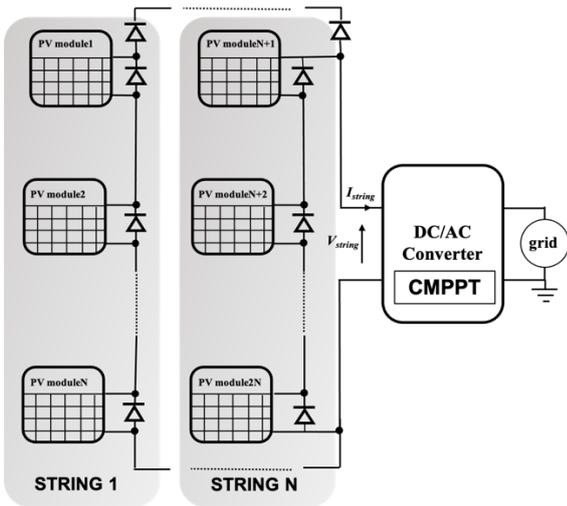


Figure 1. Commonly used grid-connected PhotoVoltaic (PV) system.

above considerations, the current study describes the realization and use of a Buck based DMPPT emulator.

The repeatability of experimental tests, the tighter control of climatic conditions, the narrowing of the gap between the physical dimensions of a PV plant and the space available in a university lab, the simplicity with which new algorithms can be tested, and the low maintenance costs are just some of the benefits offered by the emulator presented in the paper.

The proposed Buck based DMPPT emulator consists of a commercial power supply controlled by a low-cost Arduino board whose control strategy is based on a set of equations defining the mathematical model of a DMPPT device.

The present paper is organized as follows: the mathematical model of a single Buck based DMPPT unit is described in detail in Section 2; Section 3 highlights the still open issues regarding the DMPPT approach; Section 4 is dedicated to the design and description of the proposed Buck based DMPPT emulator; experiments and tests are presented in Sections 5; finally, Section 6 draws the conclusions.

2. MATHEMATICAL MODEL OF A BUCK BASED PV UNIT

In the following, the system shown in Figure 3 will be considered and analysed. It is composed of a PV module equipped with its own Buck DC/DC converter realizing the DMPPT function. To simplify the readability, the considered

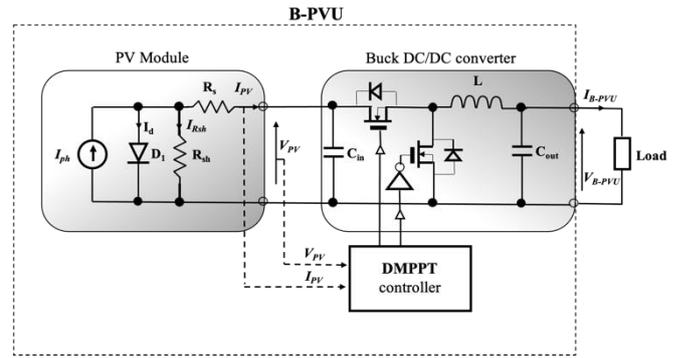


Figure 3. Circuit model of B-PVU.

system will be indicated with the acronym “B-PVU” that means Buck based PhotoVoltaic Unit.

In Figure 3, I_{PV} (V_{PV}) and I_{B-PVU} (V_{B-PVU}) denote, respectively, currents (voltages) at the input and output ports of the Buck DC/DC converter. Moreover, the symbol P_{PV} indicates the power extracted from the PV module, while the symbol P_{B-PVU} indicates the power at the output of the converter. The PV module is described by a single diode model, in which: the current generator represents the photo-induced current, the diode D_1 takes into account the effects at the silicon p–n junction of the PV cell; the series (R_s) and parallel (R_{sh}) resistances take into account the loss mechanisms taking place in the PV module due to metallic ribbon. In Figure 4, the typical output static Current vs. Voltage (I - V) characteristics of a PV module (dashed line) and of a B-PVU (bold line) are reported, at constant irradiance (S) and temperature (T) values. Losses occurring in the power stage of the Buck converter (switching, conduction, and iron losses) and the settling time of the step response of a closed or open loop B-PVU are neglected. In addition, the MPPT efficiency of the DMPPT controllers is supposed to be equal to one ($\eta_{DMPPT} = 1$). In these hypotheses, the output static I - V characteristic of a single B-PVU is marked by the presence of three different operating regions: Best Operating Region (BOR), and two Worst Operating Regions (WOR1 and WOR2). The adjectives best and worst are not used randomly insofar as they allow to discriminate operating points with high efficiency with respect to the other ones.

Worst Operating Region 1 (WOR1): In the WOR1, defined for $0 V \leq V_{B-PVU} \leq V_1$, the output I - V characteristic of the B-PVU is flat and equal to:

$$I_{B-PVU} = I_{DSMAX} \cdot \quad (1)$$

To explain the meaning of the voltage value V_1 some preliminary considerations are necessary. Since the Buck converter is able to lower its output voltage V_{B-PVU} with respect to the input voltage V_{PV} , and by considering that, when the PV module is working in its MPP, it must be:

$$V_{B-PVU} \cdot I_{B-PVU} = P_{MPP} \quad (2)$$

where P_{MPP} is MPP power. It is evident that, as long as the PV module operating point matches the MPP, the lower the output voltage the higher the output current. By indicating with I_{DSMAX} the maximum allowed value of I_{B-PVU} without harming any silicon device (i.e. power mosfets) we get:

$$V_1 = \frac{P_{MPP}}{I_{DSMAX}} \quad (3)$$

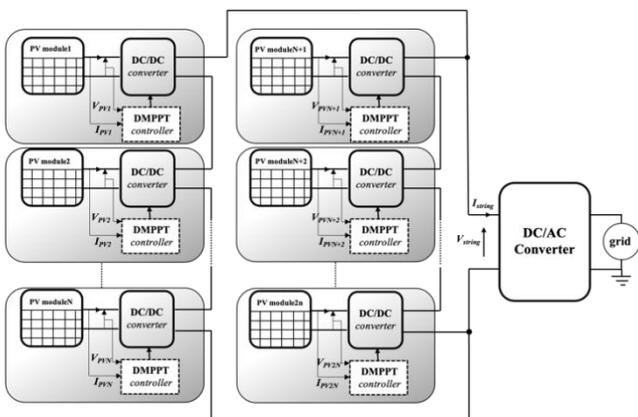


Figure 2. Commonly used DMPPT grid-connected PhotoVoltaic (PV) system.

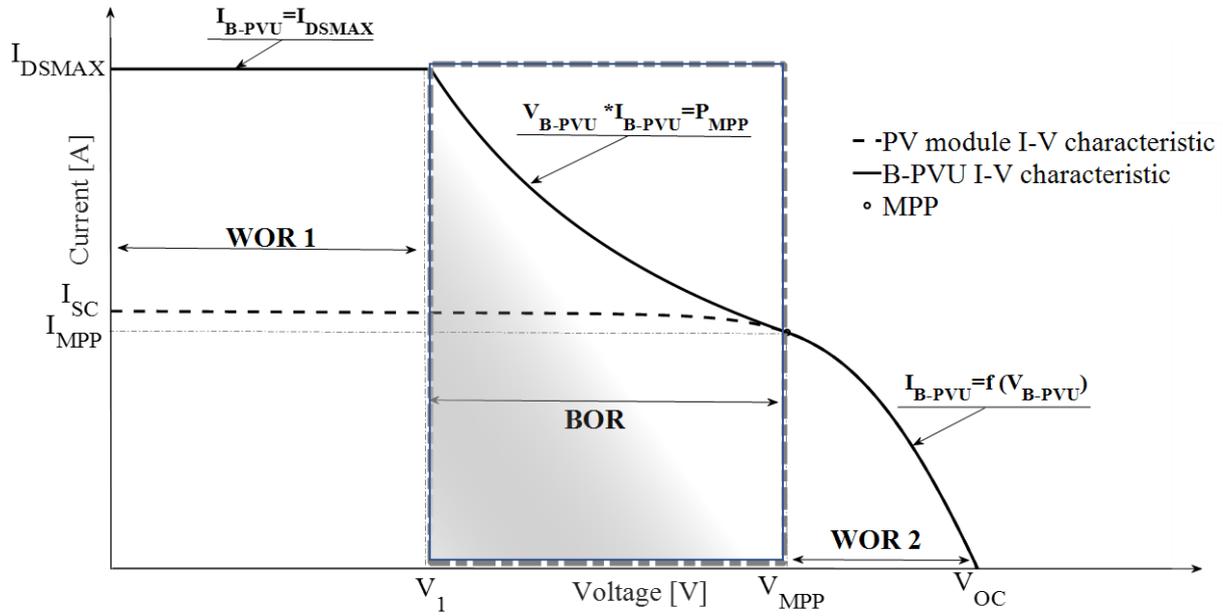


Figure 4. PV module I - V characteristic and B-PVU I - V characteristic.

Therefore, V_1 is the lower limit of the output voltage V_{B-PVU} when the output power P_{B-PVU} assumes its maximum value and it can be calculated by using (3) once I_{DSMAX} is known. In practice, the information regarding the value of I_{DSMAX} is included in the silicon devices' datasheet.

Best Operating Region (BOR): BOR, defined for $V_1 \leq V_{B-PVU} \leq V_{MPP}$ ($R_{BOR-V} = [V_1, V_{MPP}]$) is described by a hyperbole of (4), where V_{MPP} is the MPP voltage.

$$I_{B-PVU} = \frac{P_{MPP}}{V_{B-PVU}}. \quad (4)$$

As shown in Figure 4, the current range (R_{BOR-I}) associated to BOR (optimal current range) is defined as follows:

$$R_{BOR-I} = [I_{MPP}, I_{DSMAX}]. \quad (5)$$

Worst Operating Region 2 (WOR2): WOR2 is defined for $V_{MPP} \leq V_{B-PVU} \leq V_{OC}$, where V_{OC} is the open circuit voltage that can be provided by the adopted PV module in the considered atmospheric conditions:

$$V_{OC} = V_{OCSTC} \left[1 + \frac{\alpha_V}{100} (T - T_{STC}) \right], \quad (6)$$

where the V_{OCSTC} (T_{STC}) is the open circuit voltage (temperature) in the Standard Test Conditions ($S_{STC} = 1000 \text{ W/m}^2$, $T_{STC} = 25^\circ\text{C}$) and α_V is the voltage temperature coefficient. In such a region, the characteristic of the controlled PV module coincides with the PV module one. In particular, for $V_{MPP} \leq V_{B-PVU} \leq V_{OC}$, the DMPPT controller forces the Buck DC/DC converter to work with a duty cycle equal to one. In this condition the current I_{B-PVU} is equal to:

$$I_{B-PVU} = I_{PV} = I_{ph} - I_d - I_{Rsh}, \quad (7)$$

where I_{ph} is the photo-induced current, which, in accordance with (8), is linearly dependent on the irradiance level (S) and the PV module temperature (T), I_d is the current in diode D_1 (9), and I_{Rsh} is the shunt-resistor current (10):

$$I_{ph} = I_{SCSTC} \frac{S}{S_{STC}} \left(1 + \frac{\alpha_1}{100} (T - T_{STC}) \right) \quad (8)$$

$$I_d = I_{sat} \left(e^{\frac{V_{PV} + R_S I_{PV}}{V_T}} - 1 \right) \quad (9)$$

$$I_{Rsh} = \frac{V_{PV} + R_S I_{PV}}{R_{sh}}, \quad (10)$$

where V_T is the thermal voltage, α_1 is the current temperature coefficient and I_{sat} is the diode reverse bias saturation current (11):

$$I_{sat} = C T^3 e^{\left(-\frac{E_{gap}}{kT} \right)}, \quad (11)$$

where $k = 1.38 \text{ J/K}$ is the Boltzmann constant, E_{gap} is the band gap of the semiconductor material (in the following it is assumed $E_{gap} = 1.124 \text{ eV}$), and C is the temperature coefficient [46]. The I - V output characteristics of a single B-PVU are strictly dependent on the irradiance and temperature levels (12). Typical curves are shown in Figure 5.

$$T = T_{ambient} + \frac{NOCT - 20}{800} S. \quad (12)$$

Since in real environmental conditions the temperature usually changes quite slowly with respect to variation of the irradiance level occurring during the day, all subsequent results were obtained by considering a constant value of the PV module temperature equal to 57.5°C ($T = 57.5^\circ\text{C}$) which corresponds to $T_{ambient} = 25^\circ\text{C}$.

In Figure 6, the I - V output characteristics of a single B-PVU, obtained by changing both the irradiance (S) and I_{DSMAX} values are reported. From the Figure 5 and Figure 6 the non-stationarity behaviour of the BOR emerges, especially when time-varying atmospheric conditions occur. In particular, the higher the value of irradiance S , the lower the amplitude of the optimal range, assessed in terms both of the current and the voltage. (R_{BOR-I} and R_{BOR-V}). Inverse conditions occur for the value of I_{DSMAX} .

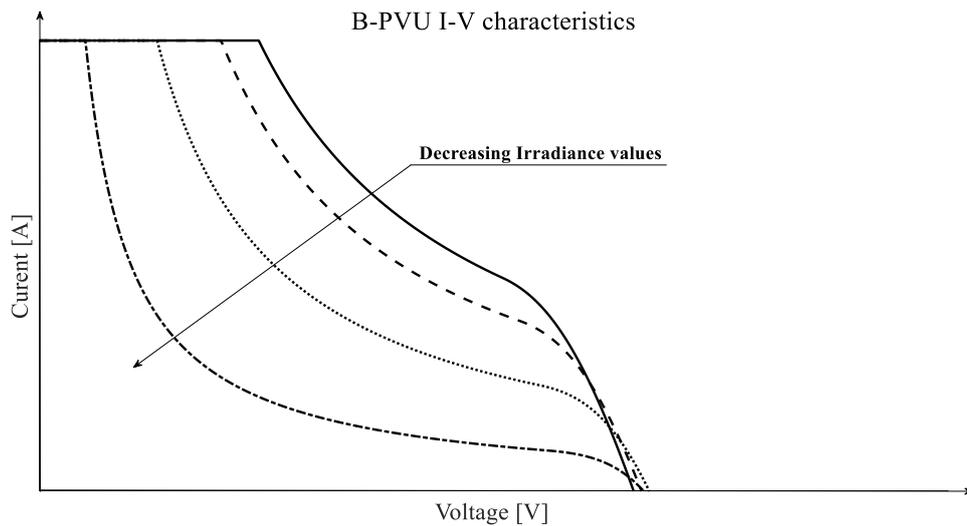


Figure 5. I-V characteristics of B-PVU.

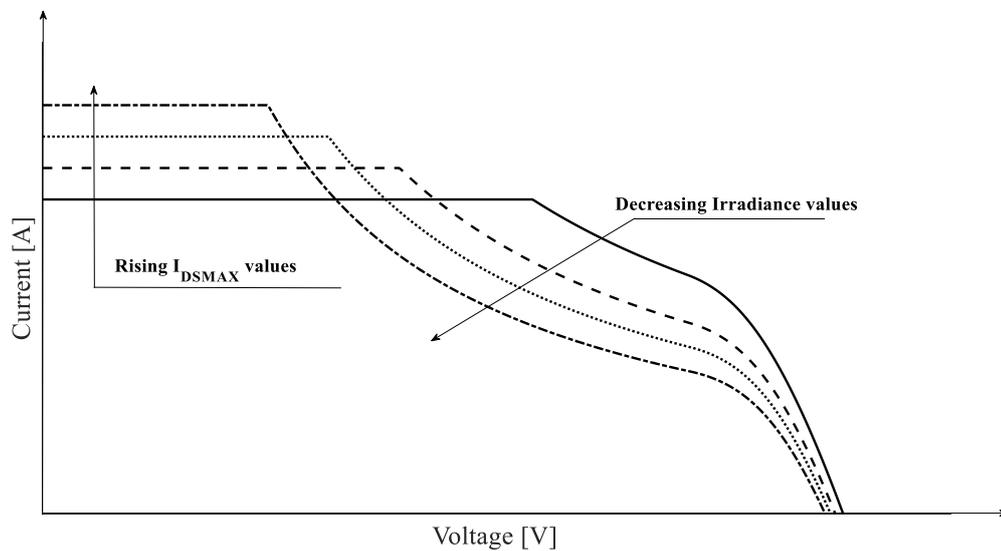


Figure 6. I-V characteristics of B-PVU.

3. OPEN ISSUES

In the previous section it has been shown that, under non-stationary operating conditions, the Best Operating Region (BOR) of a single B-PVU, whether assessed in terms of the current or the voltage, has time-varying characteristics (Figure 5 and Figure 6). Of course, the same considerations can be extended to more complex systems, which consist of many strings of B-PVUs connected in parallel. Without loss of generality, it is possible to consider, as a reference case, a PV system with a simple topology; in particular, a PV system, consisting of a single string of $N_s = 2$ B-PVUs (B-PVU1 and B-PVU2), will be analysed in the following (Figure 7). The Standard Test Conditions (STC) parameters of the considered commercial PV module (Sunmodule SW225 [47]), are reported in Table 1.

In Figure 8, the output static Power vs. Voltage ($P-V$) characteristics of the reference system are reported in three different mismatching scenarios and with three different values of I_{DSMAX} (Case I, Case II and Case III). In such cases, as shown in Figure 8, the adopted distributions of the irradiance values (S) characterizing the PV modules, the values of the maximum

allowed current (I_{DSMAX}) provided by the silicon devices and the value of the ambient temperature ($T_{ambient}$) are the following ones: $[S] = ([1000; 800])$ W/m², $I_{DSMAX} = 8$ A (Case I), $[S] = ([1000; 600])$ W/m², $I_{DSMAX} = 10$ A (Case II), $[S] = ([1000; 200])$ W/m², $I_{DSMAX} = 12$ A (Case III), $T_{ambient} = 25$ °C. It is worth clarifying that the j -th component of vector $[S]$ represents the constant irradiance value of the j -th PV module of the array ($j=1, 2$). In Figure 8, the three different Best Operating Regions, corresponding to the three different scenarios, are highlighted.

Table 1. SolarWorld SW 225 PV module electrical characteristics in STC, ($S_{STC} = 1000$ W/m²; $T_{SCT} = 25$ °C).

STC open circuit voltage	$V_{OCSTC} = 36.7$ V
STC short circuit current	$I_{SCSTC} = 8.13$ A
STC maximum power point voltage	$V_{MPPSTC} = 29.7$ V
STC maximum power point current	$I_{MPPSTC} = 7.59$ A
Voltage temperature coefficient	$\alpha_V = -0.34\% / K$
Current temperature coefficient	$\alpha_I = 0.034\% / K$
Nominal Operating Cell Temperature	$NOCT = 46$ °C

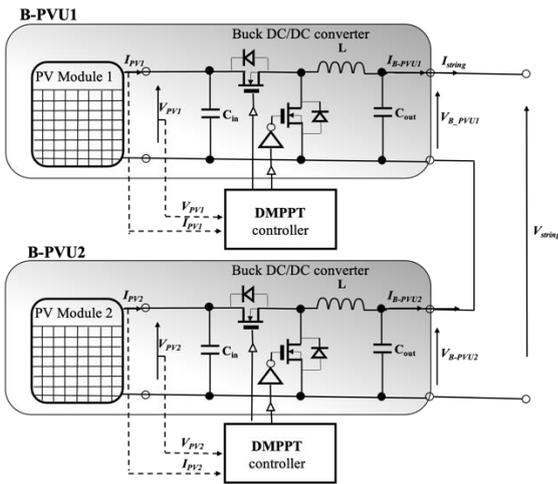


Figure 7. PV system under test.

This means that, the energetic performances of PV systems, adopting the buck based DMPPT approach, strongly depend on the value assumed by the string voltage (V_{string}), that, necessarily, must be contained in the BOR. For such a reason, the string voltage must be considered as a variable to be controlled through a proper MPPT algorithm that acts at the output of the entire PV system. This approach is known with the acronym HMPPT that means Hybrid MPPT since it is based on the joint adoption of both Central and Distributed approaches. As a further example a more complex PV system, composed of a string of $N_s = 11$ B-PVUs can be considered. The $P-V$ characteristics are plotted in Figure 9 and refer to the following parameters: $[S] = (1000; 1000; 1000; 1000; 800; 800; 800; 700; 700; 600; 600)$ W/m², $I_{DSMAX} = 8$ A (Case I), $[S] = (650; 650; 500; 500; 500; 500; 400; 400; 300; 300; 200)$ W/m², $I_{DSMAX} = 10$ A (Case II), $T_{ambient} = 25$ °C. Also in this case, for an optimal energetic performance, the BOR must be continuously tracked.

The above considerations have general validity since, as shown in [38]-[40], they do not depend on the size of PV system and/or on the type of the used DC/DC converters such as Buck, Boost and Buck-Boost. Moreover, recent studies have highlighted the possibility to combine the DMPPT and reconfiguration approaches as a powerful strategy for increasing the efficiency of PV systems [41]. This means that the opportunities offered by the DMPPT approach are still to be explored. To fully understand their benefits, it is necessary to have access to a tool, able to emulate not only the $I-V$ characteristic of a single B-PVU but also of a string of generic

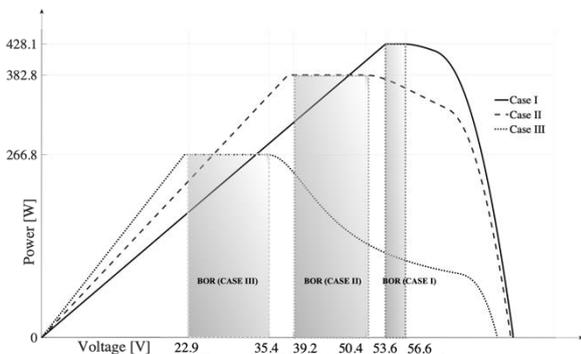


Figure 8. P-V characteristics of a string of two B-PVUs.

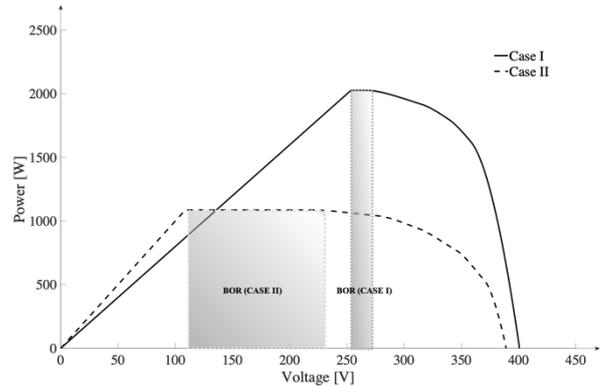


Figure 9. P-V characteristics of a string of eleven B-PVUs.

number of B-PVUs, in controlled atmospheric conditions and with different values of I_{DSMAX} . The possibility to swap the value of I_{DSMAX} allows the emulation of many Buck converters commercially available, which results in a consistent reduction in time and cost. It is expected that the proposed tool will give new impetus to the advance in PV applications since it represents a useful incentive for both development and testing of new MPPT algorithms suitable for DMPPT implementation. Moreover, it is possible to experimentally validate the performances offered by the joint adoption of DMPPT and reconfiguration approaches. The newly emerged concepts represent the main motivations that pushed the authors to the development of an experimental demonstration unit based on the Buck DC/DC converter. The proposed emulator is unique as confirmed by the absence both in literature and on the market of an equivalent device, able to emulate the $I-V$ characteristic of a DMPPT unit based on the Buck converter.

4. IMPLEMENTATION OF B-PVU EMULATOR

A block diagram of the proposed B-PVU emulator is shown in Figure 10. It consists of three fundamental blocks: Power Block (PB), Control Block (CB) and Acquisition Block (AB).

Power Block (PB): PB is realized by means of two commercial power supplies (Kepco BOP 100-4 [48]). One represents the power stage of the B-PVU emulator and is used as a current-controlled source whose output current $I_{PVU}(t)$ is regulated by means of a proper controller. The other power supply is used to obtain a controlled electronic load able to properly scan the $I-V$ characteristics of the B-PVU emulator. The two power supplies are suitable to work in all four quadrants of the current-voltage plane. They are linear power supplies with two bipolar control channels (voltage or current mode), selectable and individually controllable by either front panel controls or remote signals. The input signal of the PB ($S_{ref}(t)$) is achieved based on the following equation:

$$S_{ref}(t) = S(t) \cdot I_{B-PVU} \quad (13)$$

where $S(t)$ represents the time varying irradiance value. Concerning the value of I_{B-PVU} , it is obtained according to (1), (4) and (7).

Control Block (CB): CB consists of controlling and conditioning units. The embedded board “Arduino Mega 2560” is used as controlling unit. The “Arduino Mega 2560” is powered via a USB connection and provides 54 digital input/output pins (15 of which can be used as PWM outputs) and 16 analog inputs, and it can be programmed through Arduino IDE software [49].

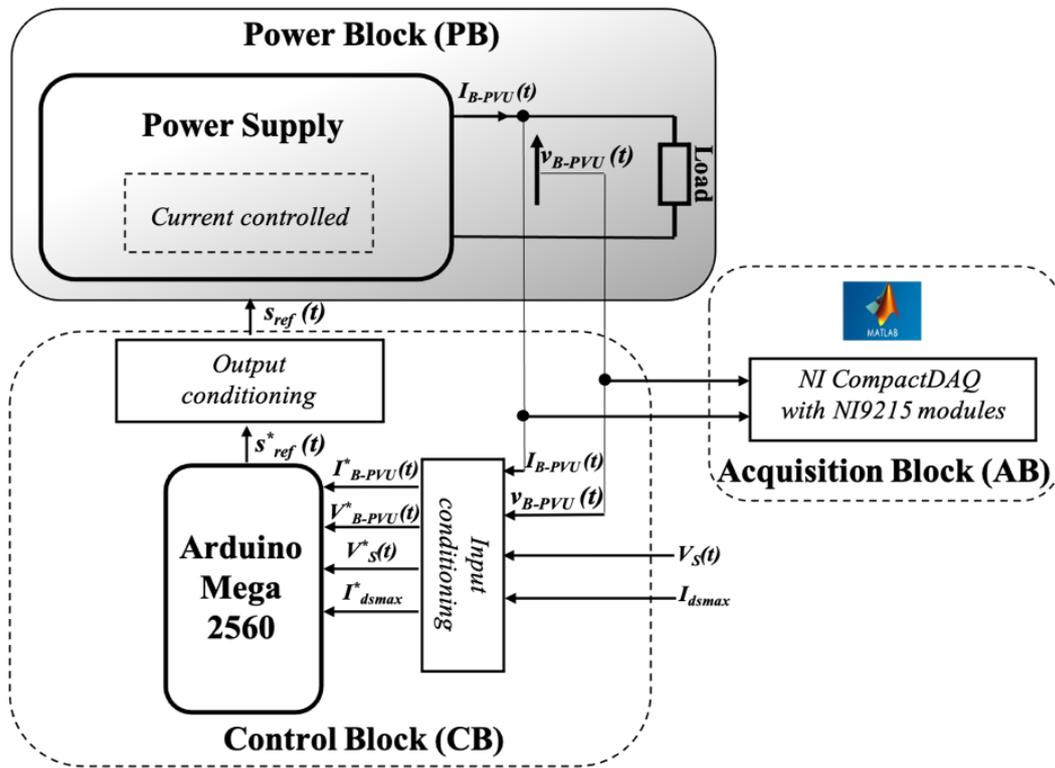


Figure 10. Block diagram of proposed B-PVU emulator.

The analog input signals of the microcontroller, which are marked with an asterisk (Figure 10), represent a scaled version of the corresponding signals $V_{B-PVU}(t)$, $I_{B-PVU}(t)$, $V_S(t)$ and I_{DSMAX} . Such scaling is necessary to adapt the electrical characteristics of such signals to the limited range [0,5] V of the microcontroller. Regarding the conditioning units, they are divided in: (a) the input conditioning unit and (b) the output conditioning unit. The input condition unit consists of a voltage (current) sensor for sensing and adapting the PVU output voltage ($V_{B-PVU}(t)$) (current $I_{B-PVU}(t)$) to the maximum allowed input voltage ($V_{B-PVU}^*(t)$ and $I_{B-PVU}^*(t)$) and a National Instruments generation board (BNC-2100 series connector blocks), which is used to reproduce the input signals $V_S^*(t)$ and I_{DSMAX}^* . To configure the data generation hardware (BNC-2100 Series Connector Blocks) MATLAB data acquisition toolbox is used.

The Arduino's input signals must fulfil the following equations:

$$V_{B-PVU}^*(t) = 5 \cdot \frac{V_{B-PVU}(t)}{V_{MAX}} \in [0, 5]V \quad (14)$$

$$I_{B-PVU}^*(t) = 5 \cdot \frac{I_{B-PVU}(t)}{I_{MAX}} \in [0, 5]V \quad (15)$$

$$V_S^*(t) = 5 \cdot \frac{\alpha_S \cdot S(t)}{S_{STC}} \in [0, 5] V \quad (16)$$

$$I_{DSMAX}^* = 5 \cdot \frac{I_{DSMAX}}{I_{MAX}} \in [0, 5] V. \quad (17)$$

where $V_{MAX} = 100$ V and $I_{MAX} = 4$ A are the maximum allowed values of the output voltage and current from the power unit: $\alpha_S = 1$ V m²/W. The adopted current sensor is an "INA169 current sensor module", which is a high-side, unipolar, current

shunt monitor, that allows the measurement of continuous current up to 5 A. To reduce the PVU output voltage up to 10 times compared to the original, a voltage divider formed by two resistances of 220 k Ω and 11.5 k Ω , respectively, was adopted as a voltage sensor. At the end, the output conditioning unit consists of a Digital Analog Converter (DAC) "Adafruit MCP4725".

Acquisition Block (AB): AB consists of a commercial National Instruments multichannel USB data acquisition system (NI CompactDAQ with NI9215 modules characterized by 16-bit resolution and maximum sampling frequency of 100 kS/s) that allows to back up the experimental data in Matlab environment.

The experimental setup was designed and built in the Circuit Laboratory of the University of Naples Federico II and is shown in Figure 11.

In Table 2, the electrical characteristics of the proposed B-PVU emulator are reported.

To verify the performances of the proposed emulator, a preliminary test activity on the system composed of a microcontroller "Arduino Mega 2560" and a DAC "MCP4725" has been carried out. In particular, two different Operating Conditions (OC1 and OC2) have been chosen, corresponding to different mismatching scenarios.

OC1 refers to the following parameters: $S = 200$ W/m², $T_{ambient} = 25$ °C, and to two different values of the maximum allowed current ($I_{DSMAX} = 4$ A and $I_{DSMAX} = 3$ A, respectively). The oscilloscope (Tektronix MDO 3034) screenshots shown in

Table 2. Electrical characteristics of the proposed B-PVU emulator.

Maximum output Current	$I_{B-PVUmax} = 5$ A
Maximum output Power	$P_{B-PVUmax} = 150$ W
Maximum output Voltage	$V_{B-PVUmax} = 36$ V

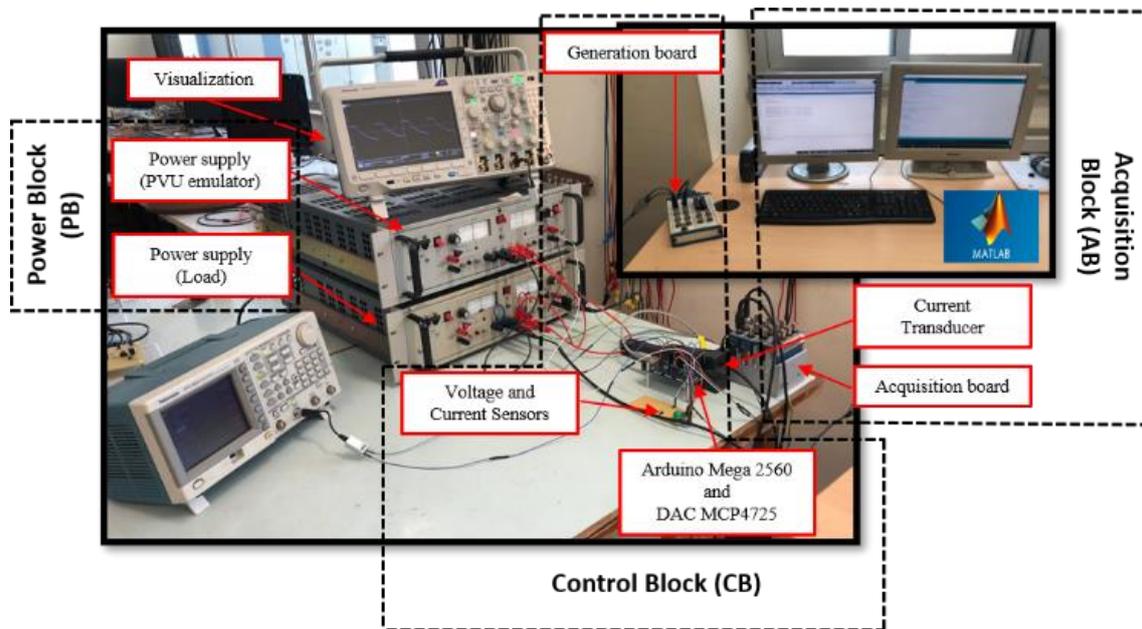


Figure 11. Experimental setup of proposed B-PVU emulator.

Figure 12 represent the (I - V) output characteristics of the emulator for $I_{DSMAX} = 4$ A (turquoise curve) and for $I_{DSMAX} = 3$ A (pink curve). The I - V characteristic of the proposed B-PVU emulator have been scanned by applying a periodic (frequency 1 Hz) ramp signal (yellow curve of Figure 12) at the microcontroller input $V_{B-PVU}^*(t)$. The amplitude of the adopted ramp signal varies from 0 V to 1.66 V which corresponds to the B-PVU ramp voltage $V_{B-PVU}(t)$ ranging from 0 V to the open circuit voltage V_{OC} at $T = 57.5$ °C ($V_{OC} = 33$ V in (12)).

The comparison of the experimental characteristic obtained when $I_{DSMAX} = 4$ A (turquoise curve) with the theoretical one (white curve) shown in Figure 13 confirms the excellent capability of the emulator.

The output characteristics have also been obtained in a different Operating Condition (OC2). It refers to the following parameters: $T_{ambient} = 25$ °C, $I_{DSMAX} = 3$ A and to two different values of irradiance ($S = 300$ W/m² and $S = 200$ W/m², respectively). The oscilloscope screenshots of Figure 14 represent the (I - V) characteristics when $S = 300$ W/m² (turquoise curve) and $S = 200$ W/m² (pink curve).

To fully explore the potentialities of the proposed emulator, in Figure 15, the time-domain behaviour of the signal S_{ref}^* ,

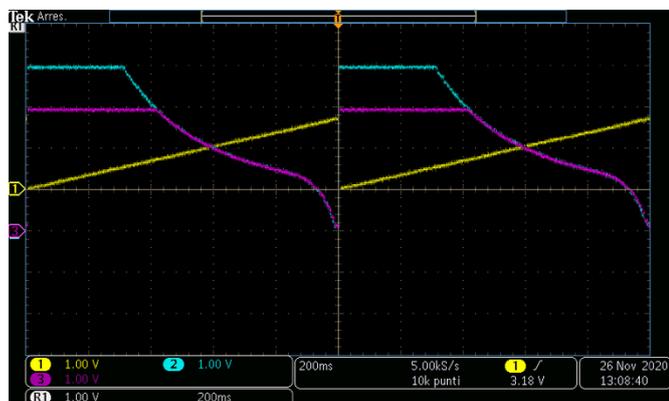


Figure 12. Oscilloscope screenshot (V - I curve in Operating Condition OC1).

obtained by considering a PV system composed of a string of two B-PVUs is reported. In such a case the adopted distribution of the irradiance values (S) characterizing the PV modules, the values of I_{DSMAX} and the value of $T_{ambient}$ are the following: $[S] = ([300; 200])$ W/m², $I_{DSMAX} = 4$ A (turquoise curve), $I_{DSMAX} = 3$ A (pink curve) and $T_{ambient} = 25$ °C. The amplitude of the ramp signal (yellow curve), differently from the previous cases, varies from 0 V to 3.32 V which corresponds to B-PVU ramp voltage $V_{B-PVU}(t)$ ranging from 0 to the open circuit voltage V_{OC} at $T = 57.5$ °C of the considered string ($V_{OC} = 66$ V).

In Figure 16 the experimental I - V curves (black lines), acquired at the output port of the Power Block, are reported together with the theoretical ones (white lines) when $I_{DSMAX} = 4$ A.

From the Figures it can be observed that the experimental and theoretical curves are nearly superimposed, thus confirming the excellent capability of the proposed solution of emulating B-PVU behavior.

At the end, a Montecarlo analysis has been performed on the model, considering the irradiance measurement affected by an uncertainty (gaussian probability density function) equal to the

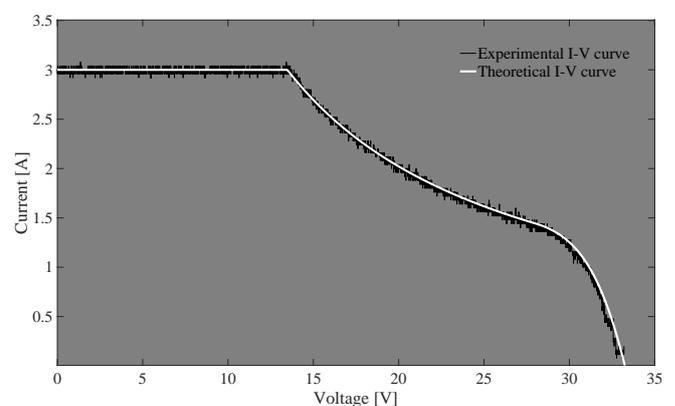


Figure 13. Comparison between experimental and theoretical I - V curves: $S = 200$ W/m², $T_{ambient} = 25$ °C, $I_{DSMAX} = 4$ A.

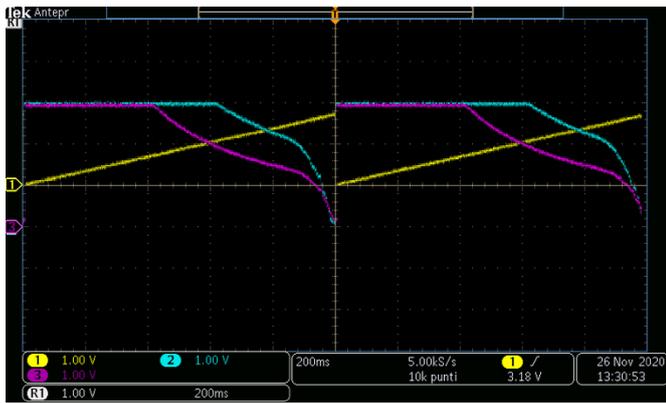


Figure 14. Oscilloscope screenshot (V-I curve in Operating condition OC2).

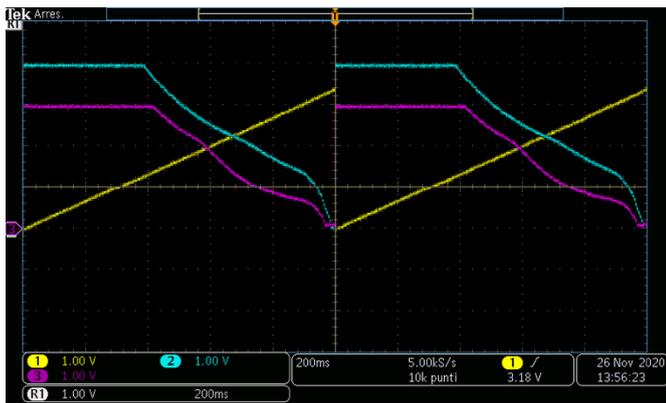


Figure 15. Oscilloscope screenshot (V-I curve in a string of two B-PVUs).

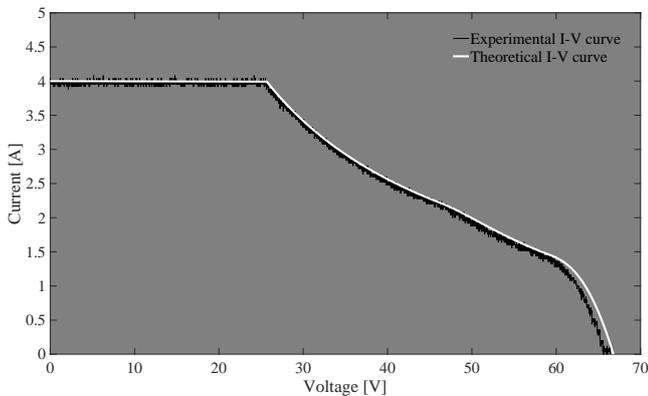


Figure 16. Comparison between experimental and theoretical I - V curves: $S = (300, 200) \text{ W/m}^2$, $T_{\text{ambient}} = 25 \text{ }^\circ\text{C}$, $I_{\text{DSMAX}} = 4 \text{ A}$.

5 % of the reading, according to pyranometer datasheet available on the market. Moreover, a tolerance (uniform probability density function) equal to 3 % has been associated with the value of I_{DSMAX} ; this value has been set according to the power tolerance declared in the specifications of the PV module SolarWorld SW 225. For each quantity 10000 realizations have been drawn according to the considered pdfs. The corresponding output of the maximum power point current I_{MPP} has been achieved; the corresponding experimental pdf is shown in Figure 17. The observed mean value is equal to 7.60 A and the standard uncertainty is 0.38 A (about 5 %).

In Figure 18 the I - V characteristics of B-PVU are shown. In particular, the portion of I - V plain corresponding to the considered input quantities variability can be appreciated.

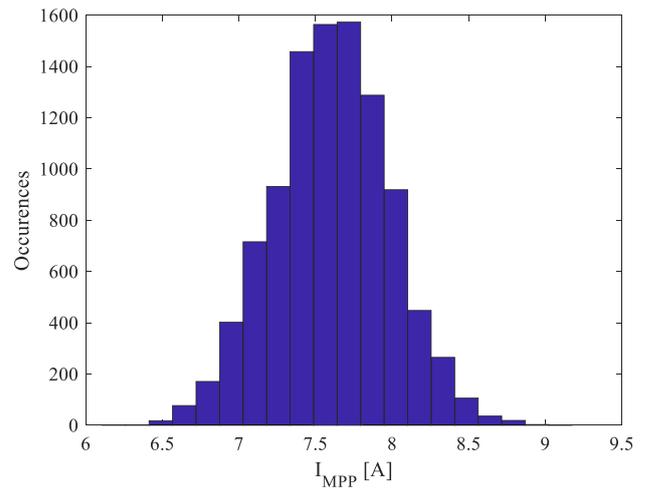


Figure 17. Observed experimental pdf of I_{MPP} in Monte Carlo analysis.

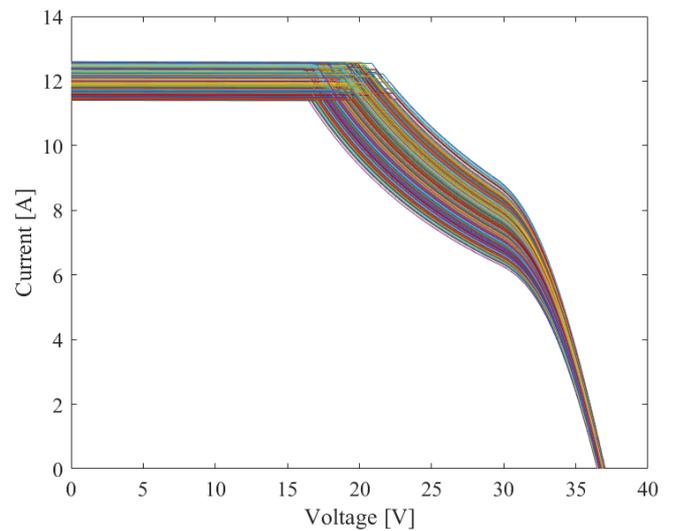


Figure 18. Set of I - V characteristics obtained in Monte Carlo analysis.

5. CONCLUSION

In this paper, a Buck based DMPPT emulator has been presented and discussed. A detailed study was carried out in order to clearly understand the set of equations on which the mathematical model of the B-PVU is based on. The proposed emulator was designed to reproduce the I - V characteristics at different values not only of the irradiance levels but also of the value of I_{DSMAX} . Moreover, the possibility to emulate a string of B-PVU is also provided.

The high flexibility offered by the proposed solution allows to fully explore the performance of the DMPPT approach in academic laboratories. Moreover, the possibility to swap the value of I_{DSMAX} allows the emulation of many Buck converters, which results in a consistent reduction in time and cost. In particular, the proposed device represents a suitable compromise between time and cost insofar as the inexpensive choice to adopt a commercial power supply is compensated by the possibility to emulate the behaviour of many commercial devices. The proposed experimental results fully confirm the validity of the proposed emulator.

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